
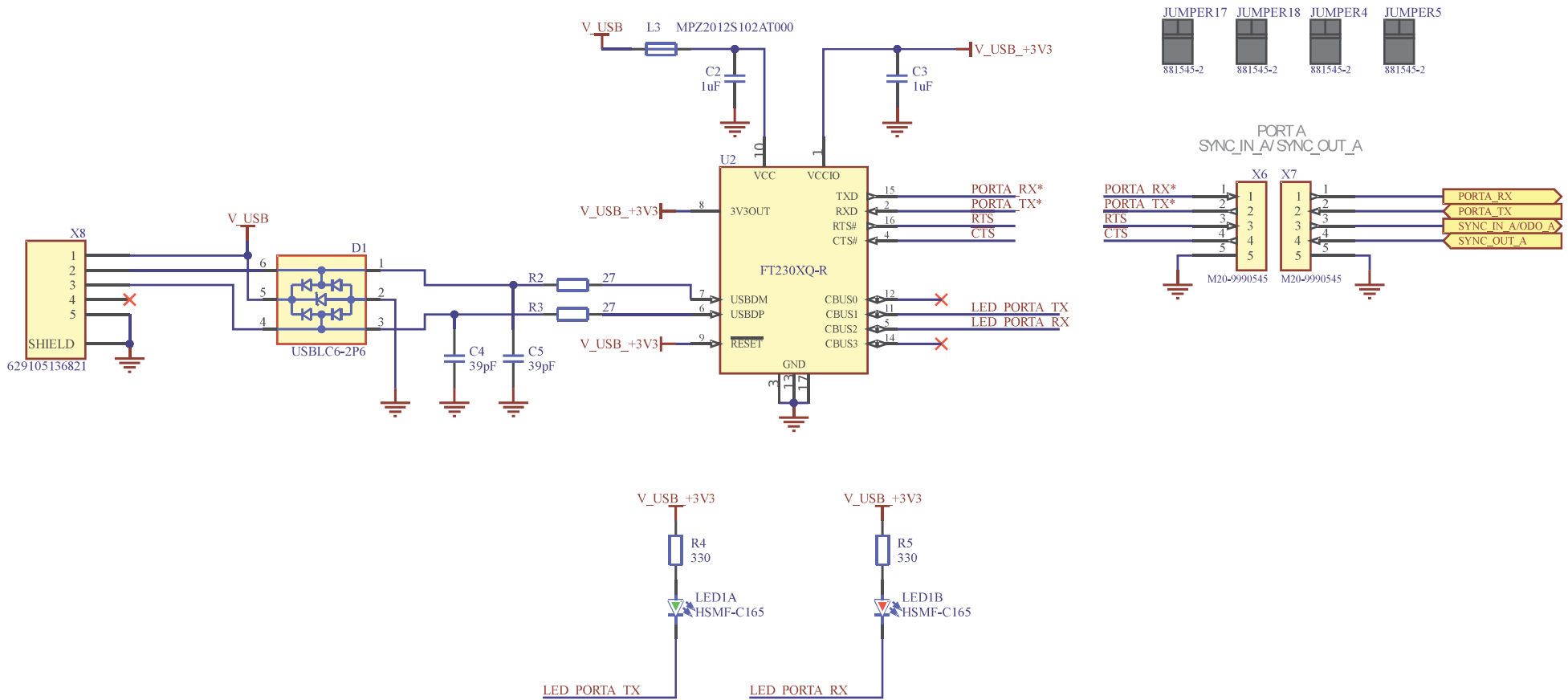
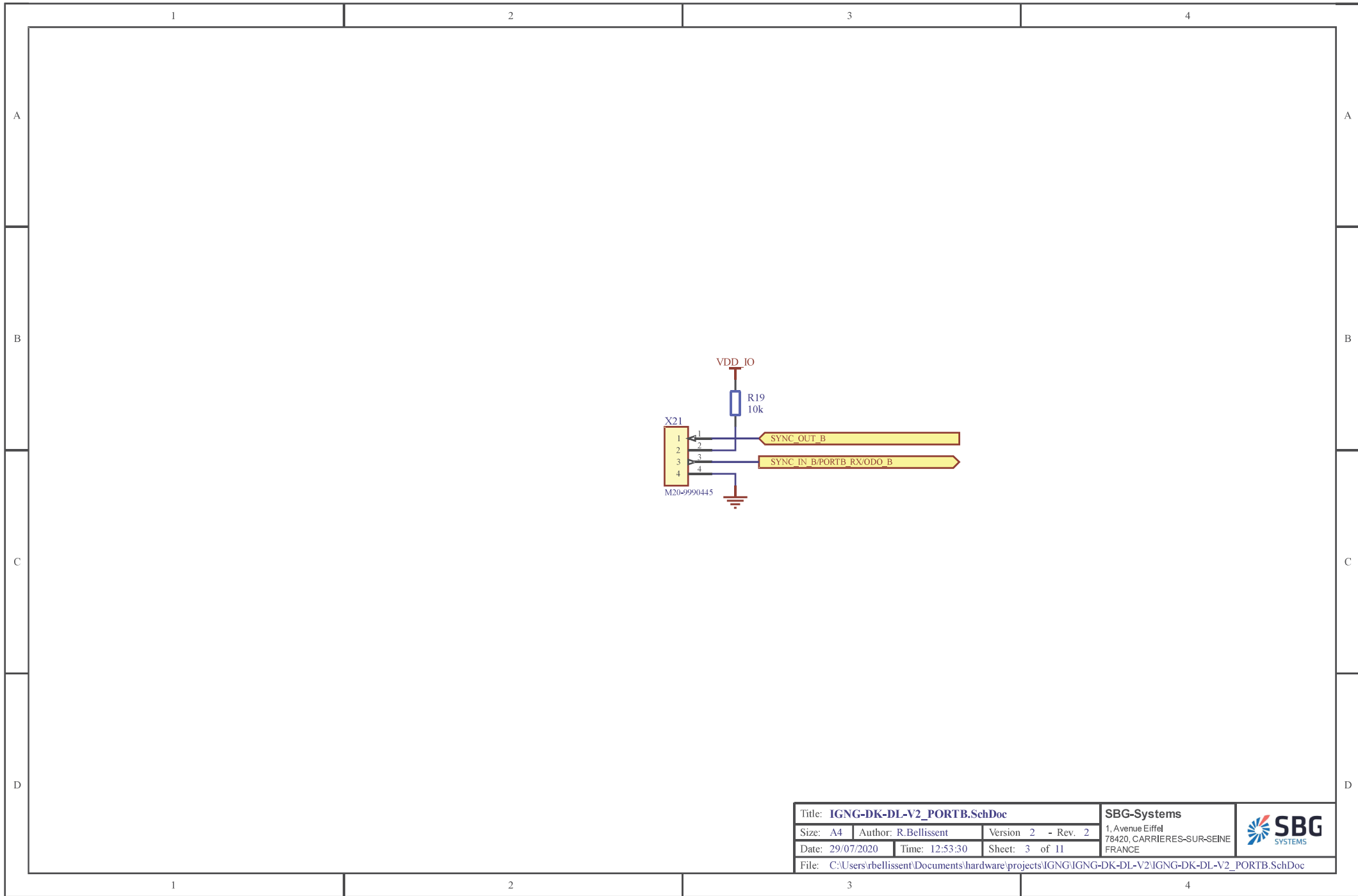



Title: CS-IGNG-IPU_DF40HC(3.0)-40DS-0.4V(51).SchDoc			SBG-Systems	
Size: A4	Author: R.Bellissent	Version 2 - Rev. 2	1, Avenue Eiffel 78420, CARRIERES-SUR-SEINE FRANCE	
Date: 09/09/2020	Time: 17:01:11	Sheet: 11 of 11		
File: C:\Users\rbellissent\Documents\hardware\common\altium\device sheets\SBG-IGNG-IPU\CS-IGNG-IPU_DF40HC(3.0)-40DS-0				



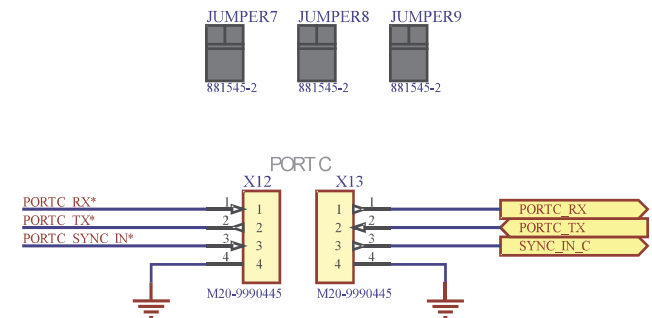
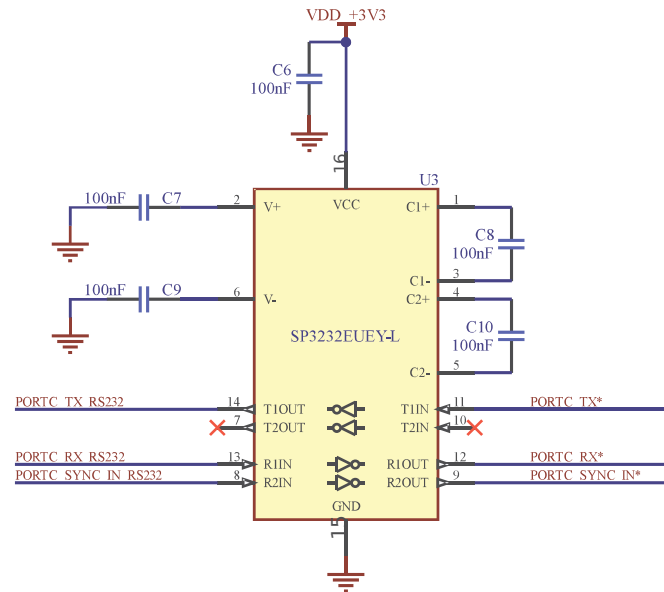
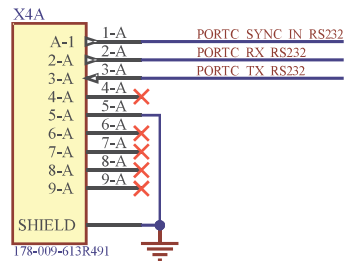
Title: IGNG-DK-DL-V2_PORTA_USB.SchDoc			SBG-Systems	
Size: A4	Author: R.Bellissent	Version 2 - Rev. 2	1, Avenue Eiffel	
Date: 29/07/2020	Time: 12:53:30	Sheet: 2 of 11	78420, CARRIERES-SUR-SEINE	
File: C:\Users\rbellissent\Documents\hardware\projects\IGNG\IGNG-DK-DL-V2\IGNG-DK-DL-V2_PORTA_USB.SchDoc			FRANCE	





Title: IGNG-DK-DL-V2_PORTB.SchDoc			SBG-Systems 1, Avenue Eiffel 78420, CARRIERES-SUR-SEINE FRANCE		
Size: A4	Author: R.Bellissent	Version 2 - Rev. 2			
Date: 29/07/2020	Time: 12:53:30	Sheet: 3 of 11			
File: C:\Users\rbellissent\Documents\hardware\projects\IGNG\IGNG-DK-DL-V2\IGNG-DK-DL-V2_PORTB.SchDoc					

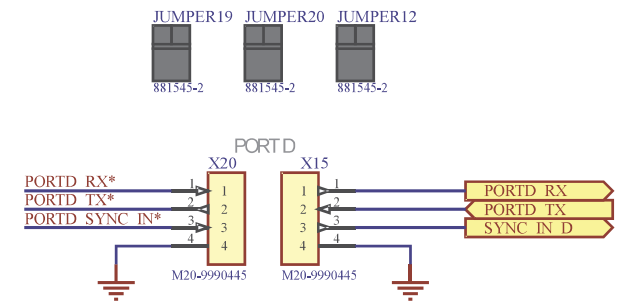
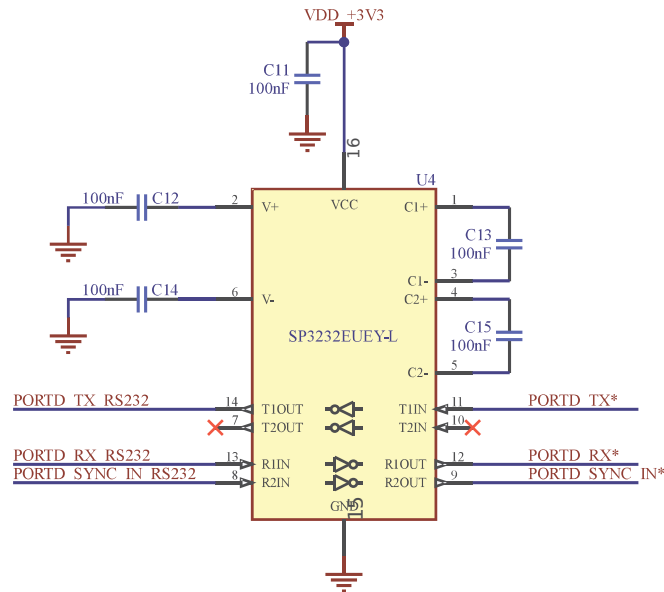
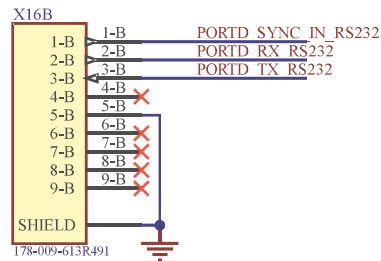
PORT C
Sub-D 9 Male Connector
with RS232 interface




Title: IGNG-DK-DL-V2_PORTC.SchDoc			SBG-Systems	
Size: A4	Author: R.Bellissent	Version 2 - Rev. 2	1, Avenue Eiffel	
Date: 29/07/2020	Time: 12:53:30	Sheet: 4 of 11	78420, CARRIERES-SUR-SEINE	
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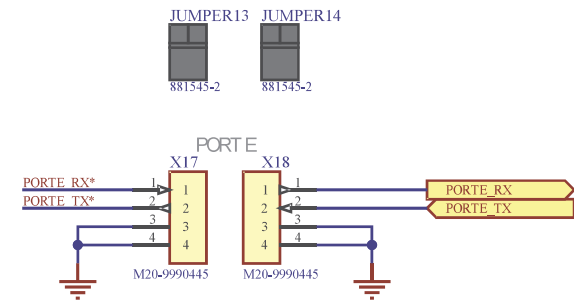
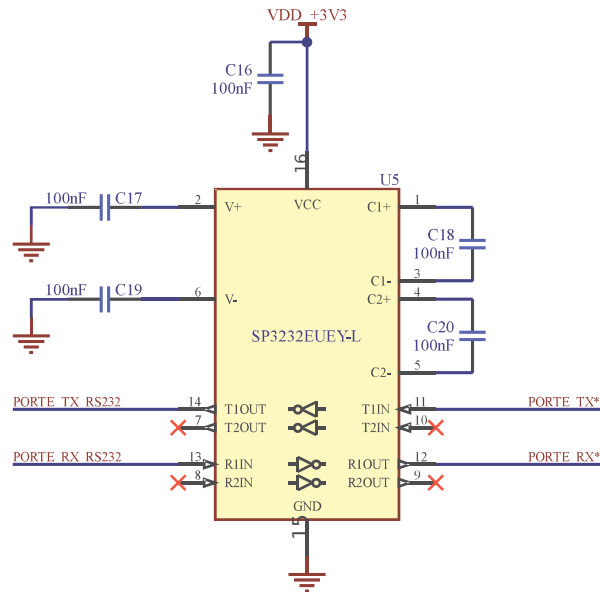
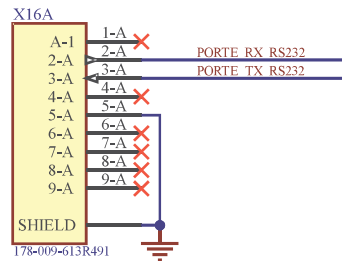


**PORT D
Sub-D 9 Male Connector
with RS232 interface**



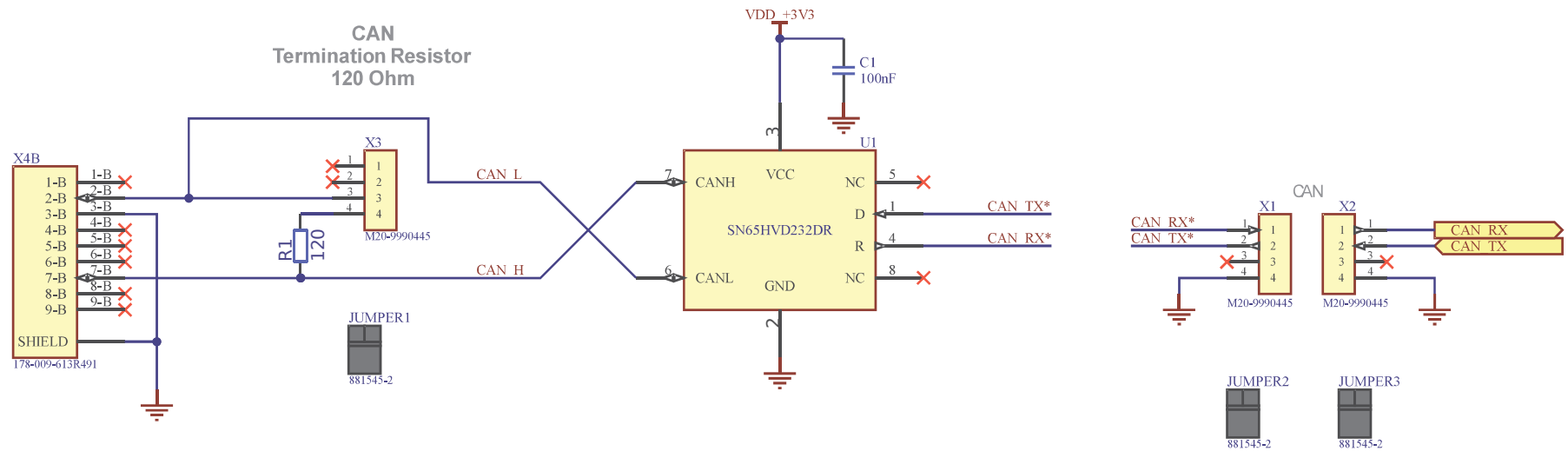
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Size: A4	Author: R.Bellissent	Version 2 - Rev. 2			
Date: 29/07/2020	Time: 12:53:30	Sheet: 5 of 11			
File: C:\Users\rbellissent\Documents\hardware\projects\IGNG\IGNG-DK-DL-V2\IGNG-DK-DL-V2_PORTD.SchDoc					

**PORT E
Sub-D 9 Male Connector
with RS232 interface**



Title: IGNG-DK-DL-V2_PORTE.SchDoc			SBG-Systems	
Size: A4	Author: R.Bellissent	Version 2 - Rev. 2	1, Avenue Eiffel	
Date: 29/07/2020	Time: 12:53:30	Sheet: 6 of 11	78420, CARRIERES-SUR-SEINE	
File: C:\Users\rbellissent\Documents\hardware\projects\IGNG\IGNG-DK-DL-V2\IGNG-DK-DL-V2_PORTE.SchDoc			FRANCE	

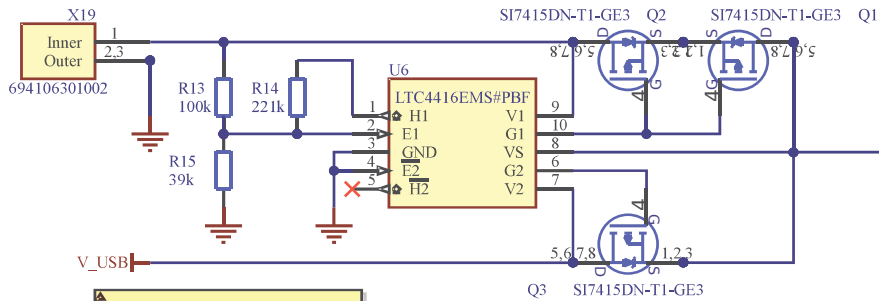




Title: IGNG-DK-DL-V2_CAN.SchDoc			SBG-Systems 1, Avenue Eiffel 78420, CARRIERES-SUR-SEINE FRANCE		
Size: A4	Author: R.Bellissent	Version 2 - Rev. 2			
Date: 29/07/2020	Time: 12:53:30	Sheet: 7 of 11			
File: C:\Users\rbellissent\Documents\hardware\projects\IGNG\IGNG-DK-DL-V2\IGNG-DK-DL-V2_CAN.SchDoc					

Vin 5- 36 V

X19
Inner
Outer
694106301002



VE1 > 1.25V for V1 activated
When Vin = 5V

R13 = 100k
R15 = 39k (39k)
VE1 = 1.4V

$5 * (R15 / R15 + R13) = 1.40V$

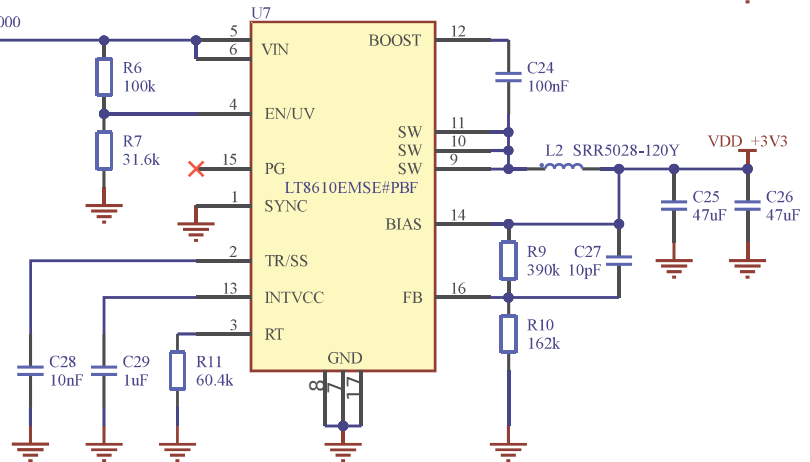
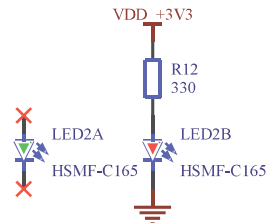
Vin enable:

$Vin_en(min) = ((R1/R2)+1) * 1 = 3.8V$

Voltage divider:

$(R2 / (R1 + R2)) * Vin = 1.31V$

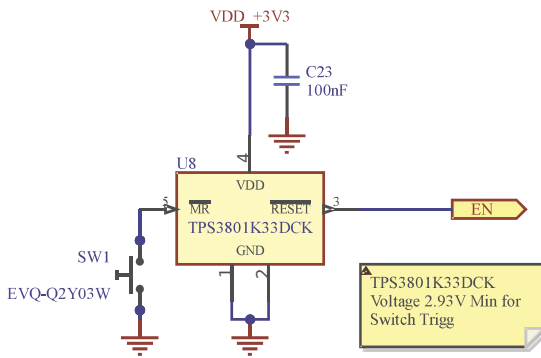
R1 = 100k
R2 = 35.7k
Vin = 5V



LT8610 : Output Voltage

$VDD +3V3 = (390k / 162k + 1) * 0.97 = 3.305V$

700 kHz : RT = 60.4kOhm



TPS3801K33DCK
Voltage 2.93V Min for
Switch Trigg

Title: IGNG-DK-DL-V2_PWR.SchDoc			SBG-Systems	
Size: A4	Author: R.Bellissent	Version 2 - Rev. 2	1, Avenue Eiffel	
Date: 29/07/2020	Time: 12:53:30	Sheet: 10 of 11	78420, CARRIERES-SUR-SEINE	
File: C:\Users\rbellissent\Documents\hardware\projects\IGNG\IGNG-DK-DL-V2\IGNG-DK-DL-V2_PWR.SchDoc			FRANCE	

